

REMARKS

Claims 1-30 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Turning now to the art rejection, Claims 1, 2, and 3 were rejected under 35 U.S.C. § 102(e) as being anticipated by Loyer; and Claims 12, 13, 16, and 20-30 were rejected under 35 U.S.C. § 103 as being unpatentable over Loyer in view of Hongbin Hao.

These rejections are respectfully traversed.

It is respectfully submitted that Loyer does not disclose or suggest the presently claimed invention including the composite divisor to produce a quotient and remainder, the minimum time interval based on a first divisor component corresponding to the quotient and the extended time interval corresponding to a second divisor component corresponding to the remainder.

The Examiner alleges that the step of providing a composite divisor is anticipated by elements 302 and 304 of Figure 3 of Loyer.

The Examiner's attention is directed to column 6, lines 5-15 of Loyer. At this location Loyer discloses for further processing of the output signal to generate the baud clock 203, the output signal is provided to a baud divider stage 302. In the baud divider stage 302, the particular clock source provided by the selector stage 300 is divided by the baud divisor value stored in the baud divisor register 200. Next, at an oversampling divide stage 304, the clock signal is divided by a particular factor to account for oversampling.

There is no mention of a quotient and remainder and the claimed relationship to the minimum time interval and the extended time interval.

Whether or not Hongbin Hao discloses generating divisor values based on the length of the start bit and one of one of ordinary skill in the art would consider modifying Loyer is of no moment since the resulting construction would still in no way disclose or suggest the presently claimed invention.

Applicants appreciate the indication that if Claims 4-11, 14, 15, and 17-19 were rewritten in independent form including the limitations of the base claim and any intervening claims, these claims would be allowable.

By the instant amendment, Claims 4, 6, 8, 10, 14, 16, and 17 have been placed in independent form including the limitations of the base claim and any intervening claims.

It is respectfully submitted that these claims are now allowable.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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